



EuroHPC JOINT UNDERTAKING
DECISION OF THE GOVERNING BOARD OF THE EuroHPC JOINT
UNDERTAKING
No 34/2025

**Approving the proposals selected for funding pursuant to the evaluation
of the Call for Proposals for the Regular Access Call (Tenth Cut-Off)**

THE GOVERNING BOARD OF THE EUROHPC JOINT UNDERTAKING,

Having regard to Council Regulation (EU) 2021/1173 of 13 July 2021 on establishing the European High Performance Computing Joint Undertaking and repealing Regulation (EU) 2018/1488¹, (hereinafter, “the JU Regulation”), in particular Articles, 16, 17 and 38(1) thereof,

Having regard to Regulation (EU) 2018/1488, and in particular Article 13 thereof,

Having regard to the Council Regulation (EU) 2024/1732 of 17 June 2024 amending Regulation (EU) 2021/1173 as regards a EuroHPC initiative for start-ups in order to boost European leadership in trustworthy artificial intelligence²,

Having regard to the Governing Board Decision No 11/2021 of 28 June 2021 amending the annual Work Plan and Budget of the EuroHPC Joint Undertaking for the year 2021³,

Having Regard to the Governing Board Decision No 18/2021 of 1 October 2021, on the Access Policy to the Union’s share on the access time to the pre-exascale and petascale supercomputers,

Having regard to the Governing Board Decision No 14/2025 of 7 April 2025 on Revision of the Access Policy of the EuroHPC Joint Undertaking Supercomputers (Amendment No 4),

¹ OJ L 256, 19.7.2021, p. 3–51

² OJ L, 19.6.2024, p. 1-5.

³ Modified by Decision of the Governing Board of the EuroHPC Joint Undertaking no 29/2021 of 14 December 2021.

Having regard to the Governing Board Decision No 19/2021 of 1 October 2021, approving the Call for Proposal and Terms of Reference for the Regular Access Call and, in particular, to Section 5(2) of its Terms of Reference,

WHEREAS

- (1) The Governing Board shall define the access rights to the Union's share of access time to the EuroHPC pre-exascale supercomputers, petascale, and high-end supercomputers and to the Union's share of access time of these supercomputers,
- (2) Pursuant to the amended Annual Work Plan 2021 adopted by Decision of the Governing Board No 11/2021 of 28 June 2021, the EuroHPC Joint Undertaking launched on 10 November 2021 the call for Proposal and Terms of Reference for the Regular Access Call,
- (3) The call for proposals (tenth cut-off) was opened on 1 December 2024 with deadline dated 28 March 2025,
- (4) By 28 March 2025, 87 proposals were submitted in response to the call of which 85 proposals were administrative accepted. The proposals requested access to 17 systems of the EuroHPC Joint Undertaking: Vega CPU, Vega GPU, MeluXina CPU, MeluXina GPU, Karolina CPU, Karolina GPU, Discoverer CPU, LUMI-C (CPU), LUMI-G (GPU), Leonardo Booster (GPU), Leonardo DCGP, MareNostrum5 ACC (Accelerated partition, GPU) and MareNostrum5 GPP (General purpose partition, CPU), Deucalion ARM, Deucalion x86, Deucalion GPU, and JUPITER Booster. From the 85 administratively accepted proposals, 77 were submitted to the Scientific track and 8 proposals to the Industry Access track,
- (5) The evaluation was performed following the Access Policy guidelines and the Terms of Reference approved by the Governing Board by Decision No 18/2021 with further amendments and Governing Board Decision No 19/2021,
- (6) The evaluation of the proposals for this call was carried out according to the rules and procedures of Horizon 2020. The proposals were verified for compliance with the admissibility requirements and eligibility conditions of the call. As a result of the evaluation, 67 applications were ranked above threshold, including 5 proposals submitted to the Industry access track and 62 proposals submitted for the Scientific Access Track. The proposals ranked above threshold were retained to receive access time to EuroHPC supercomputers,
- (7) The Governing Board is requested to approve the list of applications to receive regular access time to EuroHPC supercomputers on the basis of the tenth Regular Access Cut-Off Evaluation Report,

- (8) The Executive Director has submitted to the Governing Board for approval the list of applications attached as Annex 1,
- (9) The Governing Board took note and acknowledged the Report of the Executive Director on the outcome of the call, which includes the Regular Access Cut-Off Evaluation Report with its annexes,
- (10) The Executive Director is required to implement the decision of the Governing Board,
- (11) The Governing Board reviewed and approved the proposals selected for funding pursuant to the evaluation of the call for proposal for the Regular Access Call and

HAS ADOPTED THIS DECISION:

Article 1

With regards to the call for proposal for the Regular Access Call, the Governing Board approves the list of applications selected to receive access time to EuroHPC supercomputers, included in Annex 1.

Article 2

The Executive Director shall inform the applicants, by email, of the result of the evaluation. The successful applicants shall be contacted by the technical teams of the assigned supercomputer centre to begin the onboarding procedure. The applicants will be requested to sign an Acceptable User Policy before accessing the system.

Article 3

This Decision shall enter into force on the date of its adoption.

Done at Luxembourg, on 21 July 2025.

For the Governing Board

Rafal Duczmal

The Chair

Annex: 10th Cut-Off Regular Access – List of awarded proposals

Annex: 10th Cut-Off Regular Access – List of awarded proposals

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Proposa ID	Track	Count ry	Vega CPU	Vega GPU	MeluXi na CPU	MeluXi na GPU	Karoli na CPU	Karoli na GPU	Discover er CPU	LUMI- C	LUMI- G	Leonar do Booster	Leonar do DCGP	MN5 GPP	MN5 ACC	Deucali on ARM	Deucali on x86	Deucali on GPU	JUPITE R Booster
EHPC- REG- 2025R0 1-032	Industr y	FR	0	0	0	0	0	25,000	0	0	0		0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-038	Scientif ic	IT	0	0	0	0	0	0	0	0	0	0	80,000	0	0	0	0	0	0
EHPC- REG- 2025R0 1-043	Scientif ic	NO	0	0	0	0	0	0	0		150,00 0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-046	Scientif ic	IT	0	0	0	0	58,663	0	0	0	0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-058	Scientif ic	SE	0	0	0	0	0	0	250,000	0	0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-064	Scientif ic	IT	0	0	0	0	0	0	0	0	0		0	0	0	0	0	0	69,700
EHPC- REG- 2025R0 1-071	Scientif ic	IT	0	0	0	0	0	0	0	120,000	0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-073	Scientif ic	SE	0	0	0	0	0	0	0	0	100,00 0	0	0	0	0	0	0	0	0

Proposa ID	Track	Count ry	Vega CPU	Vega GPU	MeluXi na CPU	MeluXi na GPU	Karoli na CPU	Karoli na GPU	Discover er CPU	LUMI- C	LUMI- G	Leonar do Booster	Leonar do DCGP	MN5 GPP	MN5 ACC	Deucali on ARM	Deucali on x86	Deucali on GPU	JUPITE R Booster
EHPC- REG- 2025R0 1-103	Scientif ic	IT	0	0	0	0	0	0	0	0	0	0	89,000	0	0	0	0	0	0
EHPC- REG- 2025R0 1-104	Scientif ic	DE	0	0	0	0	0	0	0	120,000	0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-105	Scientif ic	GR	0	0	0	0	0	0	0	0	0	0	0	230,000	0	0	0	0	0
EHPC- REG- 2025R0 1-123	Scientif ic	GR	62,784	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-137	Scientif ic	UK	0	0	84,924	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-138	Scientif ic	UK	0	0	0	0	0	0	0	0	0	0	0	100,000	0	0	0	0	0
EHPC- REG- 2025R0 1-146	Scientif ic	NO	0	0	0	0	0	0	0	61,720	102,18 9	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-148	Scientif ic	IT	0	0	0	0	0	0	0	60,000	0	0	80000	0	0	0	0	0	0

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Proposa ID	Track	Count ry	Vega CPU	Vega GPU	MeluXi na CPU	MeluXi na GPU	Karoli na CPU	Karoli na GPU	Discover er CPU	LUMI- C	LUMI- G	Leonar do Booster	Leonar do DCGP	MN5 GPP	MN5 ACC	Deucali on ARM	Deucali on x86	Deucali on GPU	JUPITE R Booster
EHPC- REG- 2025R0 1-204	Scientif ic	ES	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	92,000
EHPC- REG- 2025R0 1-206	Scientif ic	IT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	57,400
EHPC- REG- 2025R0 1-207	Scientif ic	UK	0	0	0	0	0	0	0	0	20,000	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-208	Scientif ic	IT	0	0	0	0	0	0	0	0	0	0	60,000	0	0	0	0	0	0
EHPC- REG- 2025R0 1-211	Scientif ic	IE	0	0	60,000	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-212	Industr y	ES	0	0	0	0	0	0	0	0	0	0	0	0	50,625	0	0	0	0
EHPC- REG- 2025R0 1-214	Scientif ic	GR	0	0	0	0	0	0	0	0	0	0	0	100,000	0	0	0	0	0
EHPC- REG- 2025R0 1-215	Scientif ic	BE	0	0	0	0	0	0	0	0	38,400	0	0	0	0	0	0	0	0

Proposa 1 ID	Track	Count ry	Vega CPU	Vega GPU	MeluXi na CPU	MeluXi na GPU	Karoli na CPU	Karoli na GPU	Discover er CPU	LUMI- C	LUMI -G	Leonar do Booster	Leonar do DCGP	MN5 GPP	MN5 ACC	Deucali on ARM	Deucali on x86	Deucali on GPU	JUPITE R Booster
EHPC- REG- 2025R0 1-217	Scientif ic	UK	0	0	0	0	0	0	0	119,000	0	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-218	Scientif ic	DE	0	0	0	0	0	0	0	0	0	100,000	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-219	Scientif ic	ES	0	0	0	0	0	0	0	0	85,716	0	0	0	0	0	0	0	0
EHPC- REG- 2025R0 1-221	Industr y	FI	0	0	0	0	0	0	0	0	0	138,850	0	0	0	0	0	0	0
Total			230,28 4	50,00 0	697,591	168,050	224,051	80,500	470,000	1,177,2 92	561,30 5	339,850	546,000	1,242,5 35	100,00 0	0	365,214	37,840	526,654