



# **BENCHMARK CALL**

## **Information For Applicants & Awarded Projects**

The information provided below is a compilation of information that is important for applying to the Benchmark calls. The main purpose is to provide this information in a single page complementing the information provided in the EuroHPC JU [Access Policy](#).

### **Information For Applicants**

#### **ELIGIBILITY CRITERIA**

Researchers from academia, research institutes, public authorities and industry established or located in an EU Member State or in a country associated to the Digital Europe Programme or to Horizon Europe, or where applicable, organisations residing, established or located in an EU Member State or in a country associated to Horizon 2020 are eligible to apply. Researchers from public research organisations are eligible to apply as long as the project leader has an employment contract with her/his institution valid for at least 3 months after the end of the allocation period. Researchers from private companies are eligible to apply when the following criteria are fulfilled:

1. The company has its head office or substantial Research and Development (R&D) activity in EU Member State or in a Country affiliated to an EU Member State or in a country associated to the Digital Europe Programme or to Horizon Europe, or where applicable, organisations residing, established or located in an EU Member State or in a country associated to Horizon 2020.
2. The project leader has an employment contract with the organisation valid for at least 3 months after the end of the allocation period.
3. Resources awarded are devoted solely for open Research and Development (R&D) purposes.

#### **ASSESSMENT CRITERIA**

Proposals for EuroHPC JU Benchmark Access Modes will first undergo a technical assessment executed by the hosting entity experts. The assessment will evaluate the pertinence and feasibility of the request for the targeted system. Applicants must provide a detailed description of the challenges hindering the scalability of the code to higher computing performances, to facilitate the technical assessment (unless it is an AI application).

The indicative schedule of the EuroHPC JU Calls for Proposals for Benchmark Access are as follows:

| Action                         | Benchmark Access                                                                |
|--------------------------------|---------------------------------------------------------------------------------|
| Proposal submission            | Continuously open for submission. Cut-off date for review on 1st of every month |
| Review of proposals & decision | Up to 2 weeks after the cut-off date                                            |
| Access start date              | Up to 2-3 weeks after the cut-off date                                          |

The process to access resources after the communication of the decision on allocation is two-fold:

- Awardees are asked to confirm their availability and readiness to use the awarded resources during the stated period
- Access is provided after receipt of a positive reply

The allocation decision, including the type of access, start/end dates, duration, and the resources awarded is not open for negotiation.

Obligations and restrictions to awardees are applied as defined in the EuroHPC [Access Policy](#).

In case of oversubscription of a particular resource, resources will be awarded on a first-come, first served basis to suitable proposals based on the submission date until the resources are exhausted.

## **Information For Awarded Projects**

### **REPORTING**

For all awarded projects, the Principal Investigator (PI) has to submit a Final Report within 3 months of the completion of an allocation, using the proper EuroHPC JU template, with the results obtained through the access to the EuroHPC JU systems, as well as qualitative feedback on the use of the resources.

This report needs to be submitted to EuroHPC Peer-Review ([access@eurohpc-ju.europa.eu](mailto:access@eurohpc-ju.europa.eu)). Failure to submit a Final Report may disqualify future proposal submissions to EuroHPC by any member of the research group.

The templates for the Final Report are available for download:  
[Benchmark Access Final Report](#)

### **DISSEMINATION**

Applicants allow EuroHPC to publish the Final Report of the project as well as PIs and team members name, surname, affiliation and country of the institution as of one year from the end date of the allocation period. Applicants commit to collaborate with EuroHPC, upon its request, in the preparation of dissemination material.

The applicant commits to not use the project results for military purposes.

## EXTENSION

The total awarded resources (total computer time and/or expert support) cannot be changed. If applicants are unable to use the awarded resources due to a technical problem, the EuroHPC Peer Review team ([access@eurohpc-ju.europa.eu](mailto:access@eurohpc-ju.europa.eu)) must be notified as soon as possible and in any case during the allocation period.

## ACKNOWLEDGEMENT

Applicants must acknowledge EuroHPC JU in all publications that describe results obtained using EuroHPC resources. Acknowledgement should be made of the role of the HPC Centre and EuroHPC JU and of the relevant partners involved in the pilot use case for joint data storage in all publications which include the results above mentioned. Users shall use the following wording in such acknowledgement in all such papers and other publications:

*We acknowledge EuroHPC Joint Undertaking for awarding us access to [resource-name hosted by at site, country]. Use as many instances of the pattern [resource-name hosted by at site, country] as the number of systems awarded via EuroHPC. Please follow these examples:*

- Vega at IZUM, Slovenia
- MeluXina at LuxProvide, Luxembourg
- Discoverer at SofiaTech, Bulgaria
- LUMI at CSC, Finland
- Leonardo at CINECA, Italy
- MareNostrum5 at BSC, Spain
- Deucalion at MACC, Portugal
- Jupiter at JSC, Germany
- Arrhenius at NAISS, Sweden

# ANNEX 1

## EUROHPC SYSTEMS AVAILABLE AND NODE HOURS

| SYSTEM*                                                                                                                                                     | SITE<br>(COUNTRY)    | PARTITION        | PROCESSOR             | ACCELERATOR   | BENCHMARK**                            |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------|------------------|-----------------------|---------------|----------------------------------------|
| <br><b>ARRHENIUS</b>                                                       | NAISS (SE)           | Arrhenius CPU    | AMD Epyc              |               | 500                                    |
|                                                                                                                                                             |                      | Arrhenius GPU    | NVIDIA Grace          | NVIDIA Hopper | 300                                    |
| <br><b>JUPITER</b>                                                         | JSC (DE)             | JUPITER Booster  | NVIDIA Grace          | NVIDIA Hopper | 2 000                                  |
| <br><b>DEUCALION</b>                                                       | FCT (PT)             | Deucalion ARM    | Fujitsu A64FX         | -             | 2 000                                  |
|                                                                                                                                                             |                      | Deucalion x86    |                       | -             | 2 000                                  |
|                                                                                                                                                             |                      | Deucalion GPU    | AMD Epyc              | NVIDIA A100   | 200                                    |
| <br><b>MN5<br/>MARENOSTRUM</b>                                             | BSC (ES)             | MN5 NG-GPP       | NVIDIA Grace          | -             | 2 500                                  |
|                                                                                                                                                             |                      | MN5 GPP          |                       | -             | 2 500                                  |
|                                                                                                                                                             |                      | MN5 ACC          | Intel Sapphire Rapids | NVIDIA Hopper | 2 000                                  |
|                                                                                                                                                             |                      | MN5 HBM          |                       | -             | 2 000                                  |
| <br><b>LEONARDO<br/>CINECA</b>                                           | CINECA (IT)          | Leonardo DCGP    | Intel Sapphire Rapids | -             | 2 000                                  |
|                                                                                                                                                             |                      | Leonardo Booster | Intel Xeon            | NVIDIA A100   | 3 500<br>STORAGE - TIB HOURS<br>65 000 |
| <br><b>LUMI</b>                                                          | CSC (FI)             | LUMI-C           |                       | -             | 2 000                                  |
|                                                                                                                                                             |                      | LUMI-G           | AMD Epyc              | AMD Instinct  | 2 500                                  |
| <br><b>DISCOVERER</b><br><small>DATA ACCELERATION SUPERCOMPUTING</small> | Sofia Tech Park (BG) | Discoverer CPU   |                       | -             | 2 000                                  |
|                                                                                                                                                             |                      | Discoverer GPU   | AMD Epyc              | NVIDIA Hopper | 200                                    |
| <br><b>MELUXINA</b><br><small>DATA ACCELERATION SUPERCOMPUTING</small>   | LuxProvide (LU)      | MeluXina CPU     |                       | -             | 2 000                                  |
|                                                                                                                                                             |                      | MeluXina GPU     | AMD Epyc              | NVIDIA A100   | 400                                    |
|                                                                                                                                                             |                      | MeluXina FPGA    |                       | Intel Stratix | 600                                    |
| <br><b>VEGA</b><br><small>XFC</small>                                    | IZUM Maribor (SI)    | Vega CPU         |                       | -             | 2 000                                  |
|                                                                                                                                                             |                      | Vega GPU         | AMD Epyc              | NVIDIA A100   | 400                                    |

(\*The above EuroHPC systems are sorted in order to show the last available system that entered production.

\*\*The resources are displayed in node hours.)

## ANNEX 2

### BENCHMARK ACCESS PROCESS WORKFLOW

